

MSBL-120CS

LVDS Digital Buffer Adapter

IMAGE



DEFINITION

- The MSBL-120CS convert received TMD5 signal into LVDS signal.
- Output LVDS signal convert into single or dual automatically depend on connected cable up to 68 type.
- EDID save function make user can display EDID info required source.

SPECIFICATION

LVDS Output	8~16Bit Single/Dual/Quad Link Support
Maximum Permissible Frequency	* HD 60Hz : 130MHz * Full HD 120Hz : 300MHz * Full HD 60Hz : 148.5MHz
Input Connector	DVI-I
Output Connector	LVDS 68 pin
Power Consumption	100~240VAC, 50/60Hz
Dimension(WXDXH)	230 X 65 X 160mm, Weight : 0.7Kg

* All specifications are subject to change without any notice.

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