

MSBL-16CS

LVDS Digital Buffer Adapter

IMAGE



DEFINITION

- The MSBL-16CS convert received TMD5 signal into LVDS signal.
- Output LVDS signal convert into single or dual automatically depend on connected cable up to 64 type.
- EDID save function make user can display EDID info required source.

SPECIFICATION

LVDS Output	8~16Bit Single/Dual Support
Maximum Permissible Frequency	* HD : 130MHz * Full HD : 148.5MHz
Input Connector	DVI-I
Output Connector	MDR68FEL
Power Consumption	DV+5V, Max 2A
Dimension(WXDXH)	128 X 160 X 30mm, Weight : 0.4Kg
Note	* Internal & External Power convert option. * Cover noise up to 1.5M LVDS cable. * EDID Copy & Read available. (PC to LVDS buffer) * Select FHD or HD output by replace exclusive LVDS cable.

* All specifications are subject to change without any notice.

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